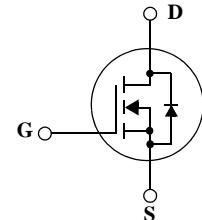
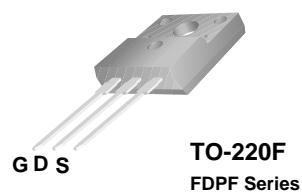
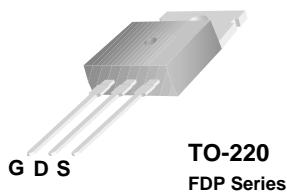


FDP33N25 / FDPF33N25T 250V N-Channel MOSFET

Features

- 33A, 250V, $R_{DS(on)} = 0.094\Omega$ @ $V_{GS} = 10\text{ V}$
- Low gate charge (typical 36.8 nC)
- Low C_{rss} (typical 39 pF)
- Fast switching
- Improved dv/dt capability



Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficient switched mode power supplies and active power factor correction.

Absolute Maximum Ratings

Symbol	Parameter	FDP33N25	FDPF33N25T	Unit
V_{DSS}	Drain-Source Voltage	250		V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) - Continuous ($T_C = 100^\circ\text{C}$)	33 20.4	33* 20.4*	A A
I_{DM}	Drain Current - Pulsed	(Note 1)	132	132*
V_{GSS}	Gate-Source voltage		± 30	V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	918	mJ
I_{AR}	Avalanche Current	(Note 1)	33	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	23.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$) - Derate above 25°C	235 1.89	37 0.29	W W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FDP33N25	FDPF33N25T	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.53	3.4	$^\circ\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	$^\circ\text{C}/\text{W}$

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP33N25	FDP33N25	TO-220	-	-	50
FDPF33N25T	FDPF33N25T	TO-220F	-	-	50

Electrical Characteristics

$T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}$, $I_D = 250\mu\text{A}$, $T_J = 25^\circ\text{C}$	250	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$, Referenced to 25°C	--	0.25	--	$^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 250\text{V}$, $V_{GS} = 0\text{V}$ $V_{DS} = 200\text{V}$, $T_C = 125^\circ\text{C}$	-- --	-- --	1 10	μA μA
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{V}$, $V_{DS} = 0\text{V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{V}$, $V_{DS} = 0\text{V}$	--	--	-100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	3.0	--	5.0	V
$R_{DS(\text{on})}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}$, $I_D = 16.5\text{A}$	--	0.077	0.094	Ω
g_{FS}	Forward Transconductance	$V_{DS} = 40\text{V}$, $I_D = 16.5\text{A}$	(Note 4)	--	26.6	--
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1.0\text{MHz}$	--	1640	2135	pF
C_{oss}	Output Capacitance		--	330	430	pF
C_{rss}	Reverse Transfer Capacitance		--	39	59	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 125\text{V}$, $I_D = 33\text{A}$ $R_G = 25\Omega$	--	35	80	ns
t_r	Turn-On Rise Time		--	230	470	ns
$t_{d(off)}$	Turn-Off Delay Time		--	75	160	ns
t_f	Turn-Off Fall Time		--	120	250	ns
Q_g	Total Gate Charge	$V_{DS} = 200\text{V}$, $I_D = 33\text{A}$ $V_{GS} = 10\text{V}$	--	36.8	48	nC
Q_{gs}	Gate-Source Charge		--	10	--	nC
Q_{gd}	Gate-Drain Charge		--	17	--	μC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current	--	--	33	A	
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current	--	--	132	A	
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{V}$, $I_S = 33\text{A}$	--	--	1.4	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0\text{V}$, $I_S = 33\text{A}$ $dI_F/dt = 100\text{A}/\mu\text{s}$	--	220	--	ns
Q_{rr}	Reverse Recovery Charge		--	1.71	--	μC

Notes:

- Repetitive Rating: Pulse width limited by maximum junction temperature
- $L = 1.35\text{mH}$, $I_{AS} = 33\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
- $I_{SD} \leq 33\text{A}$, $dI/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
- Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
- Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

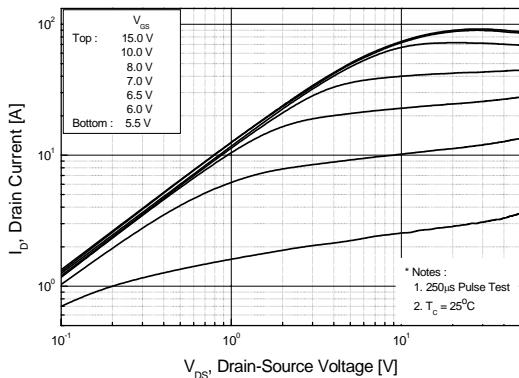


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

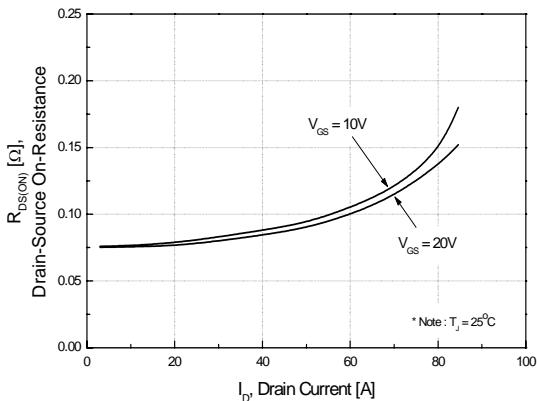


Figure 5. Capacitance Characteristics

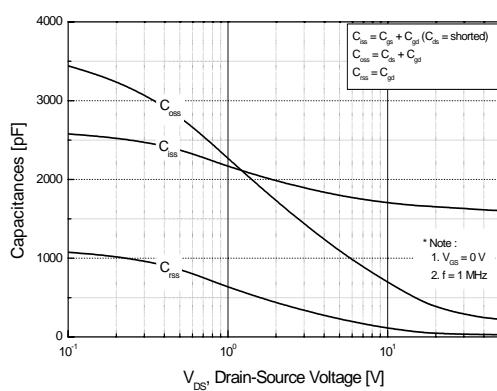


Figure 2. Transfer Characteristics

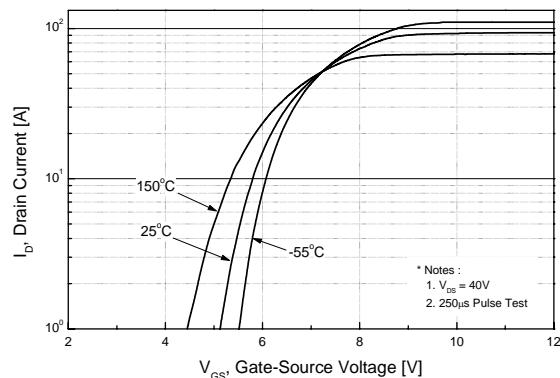


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

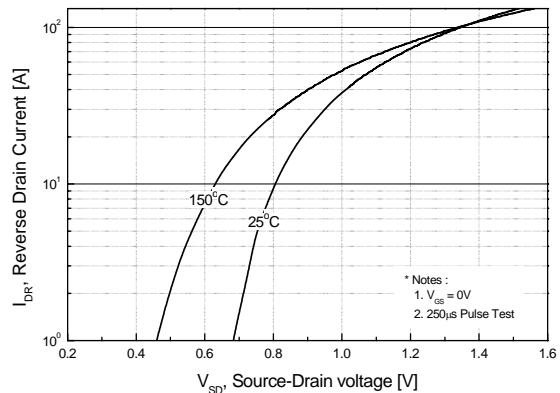
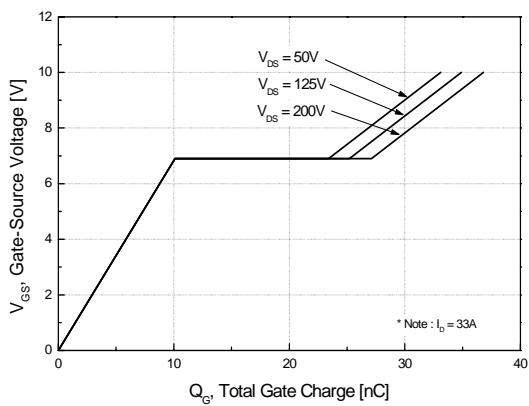


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

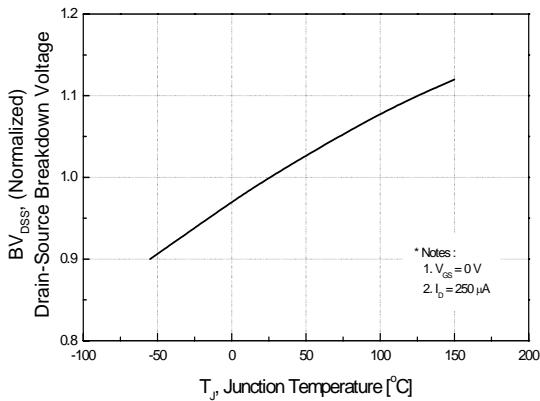


Figure 8. On-Resistance Variation vs. Temperature

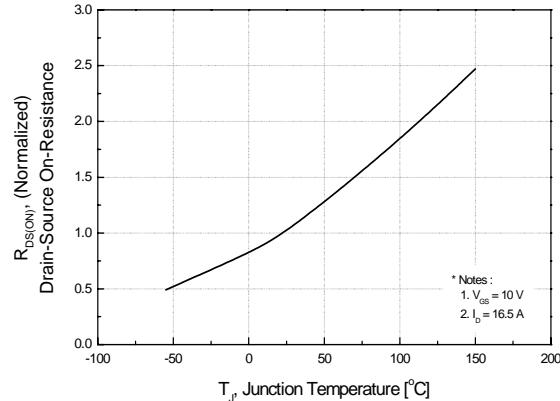


Figure 9-1. Maximum Safe Operating Area for FDP33N25

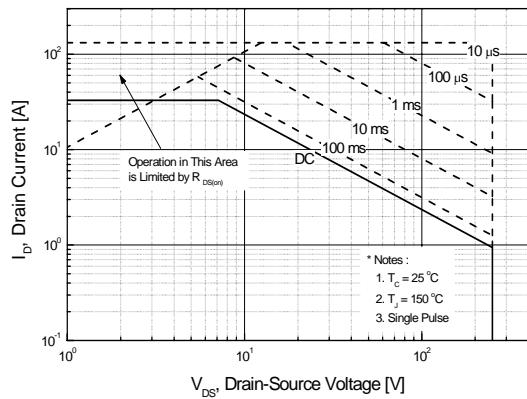


Figure 9-2. Maximum Safe Operating Area for FDPF33N25T

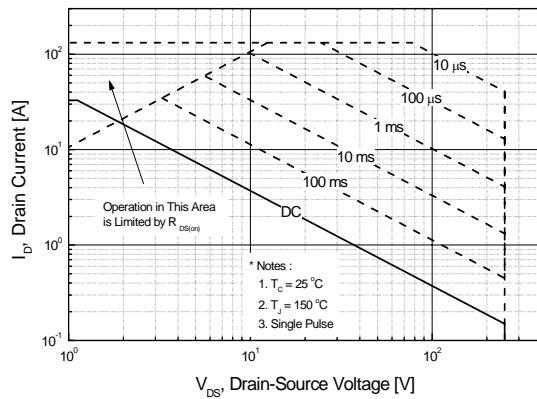
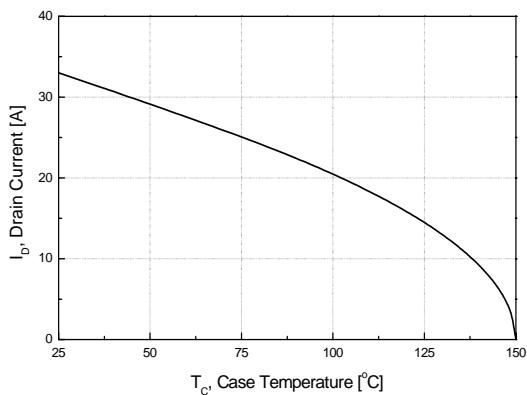


Figure 10. Maximum Drain Current vs. Case Temperature



Typical Performance Characteristics (Continued)

Figure 11-1. Transient Thermal Response Curve for FDP33N25

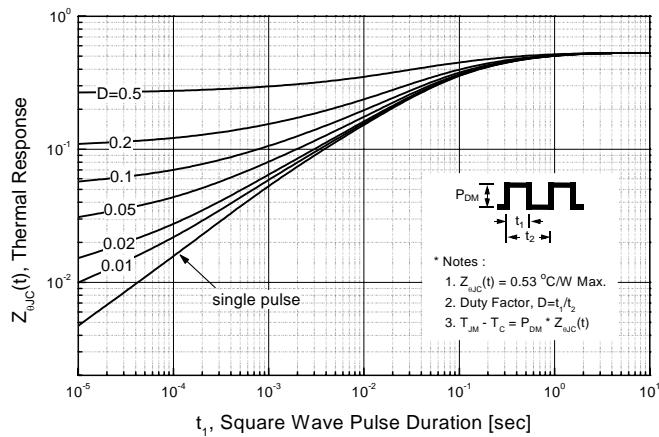
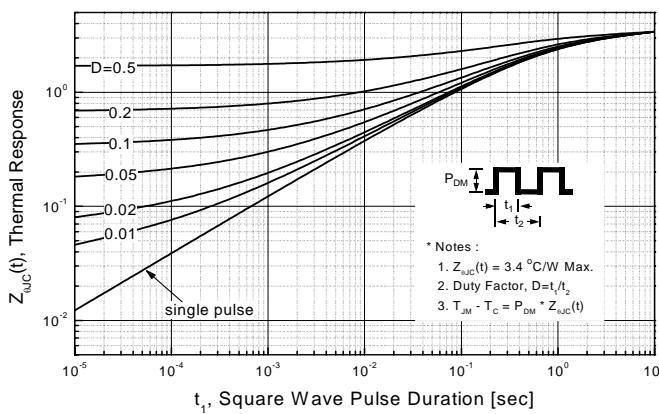
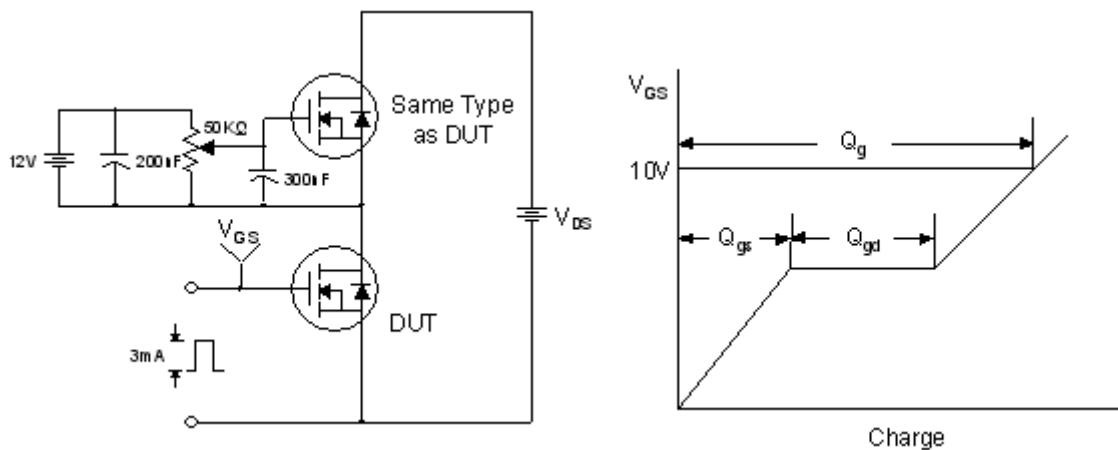


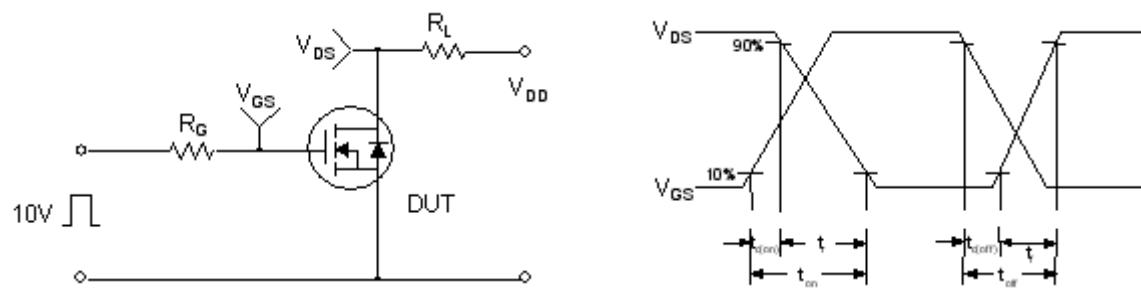
Figure 11-2. Transient Thermal Response Curve for FDPF33N25T



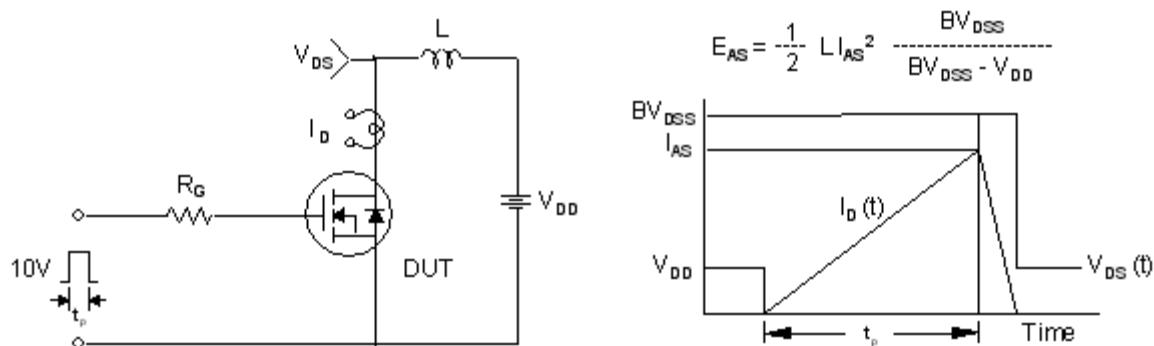
Gate Charge Test Circuit & Waveform



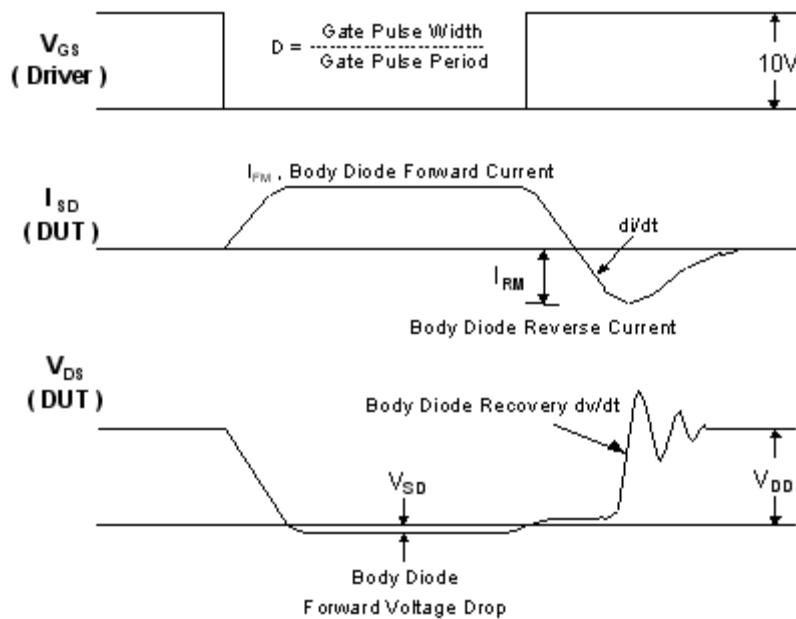
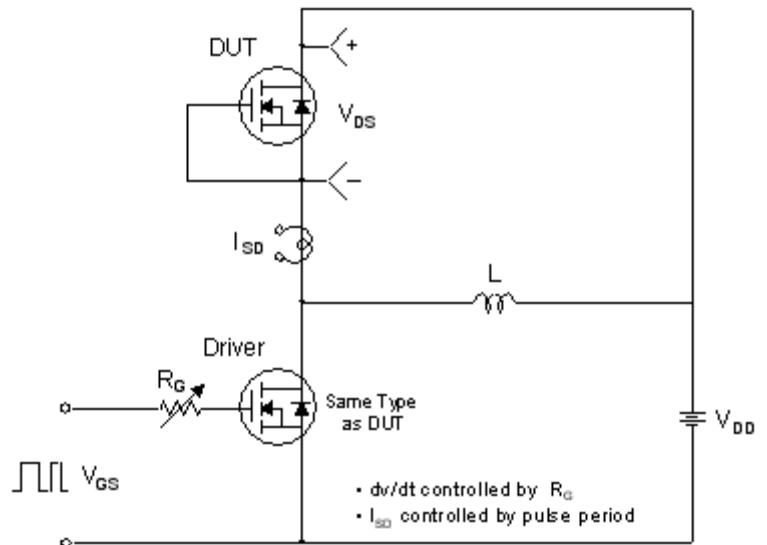
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

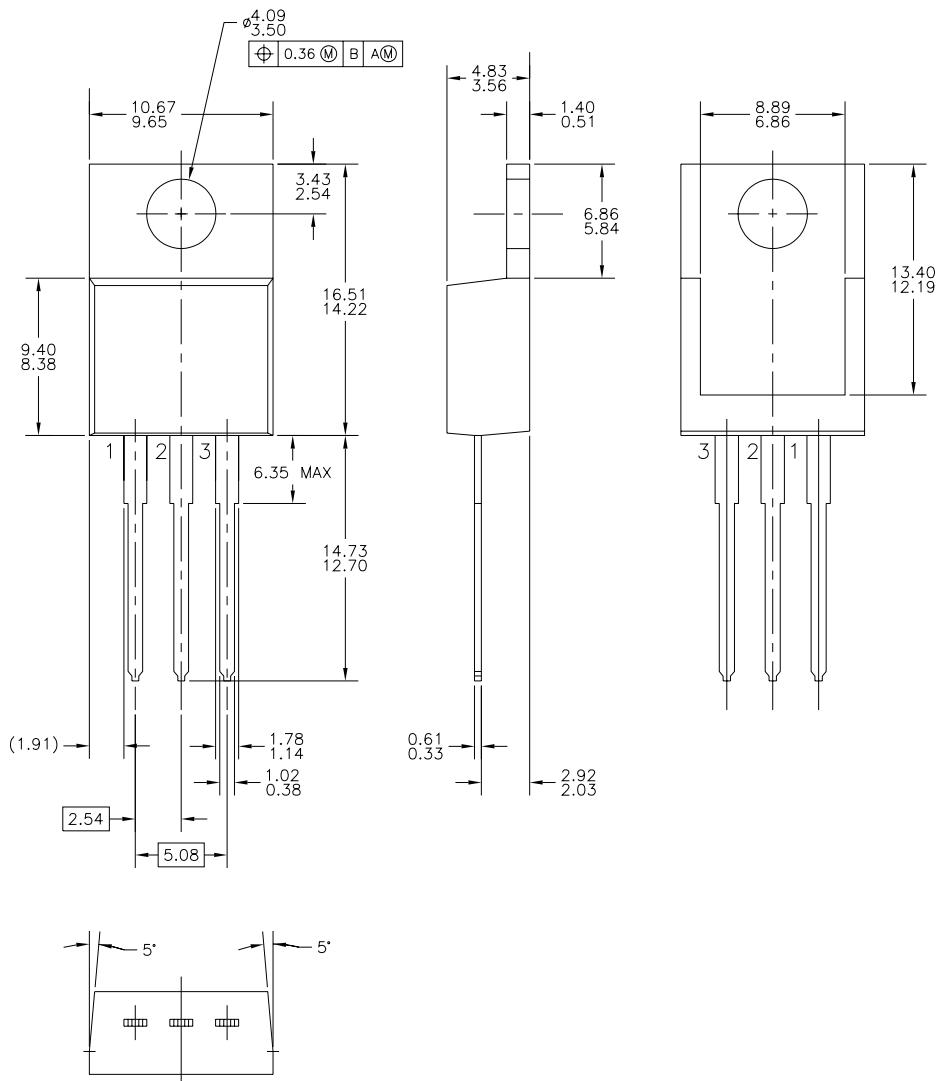


Peak Diode Recovery dv/dt Test Circuit & Waveforms



Mechanical Dimensions

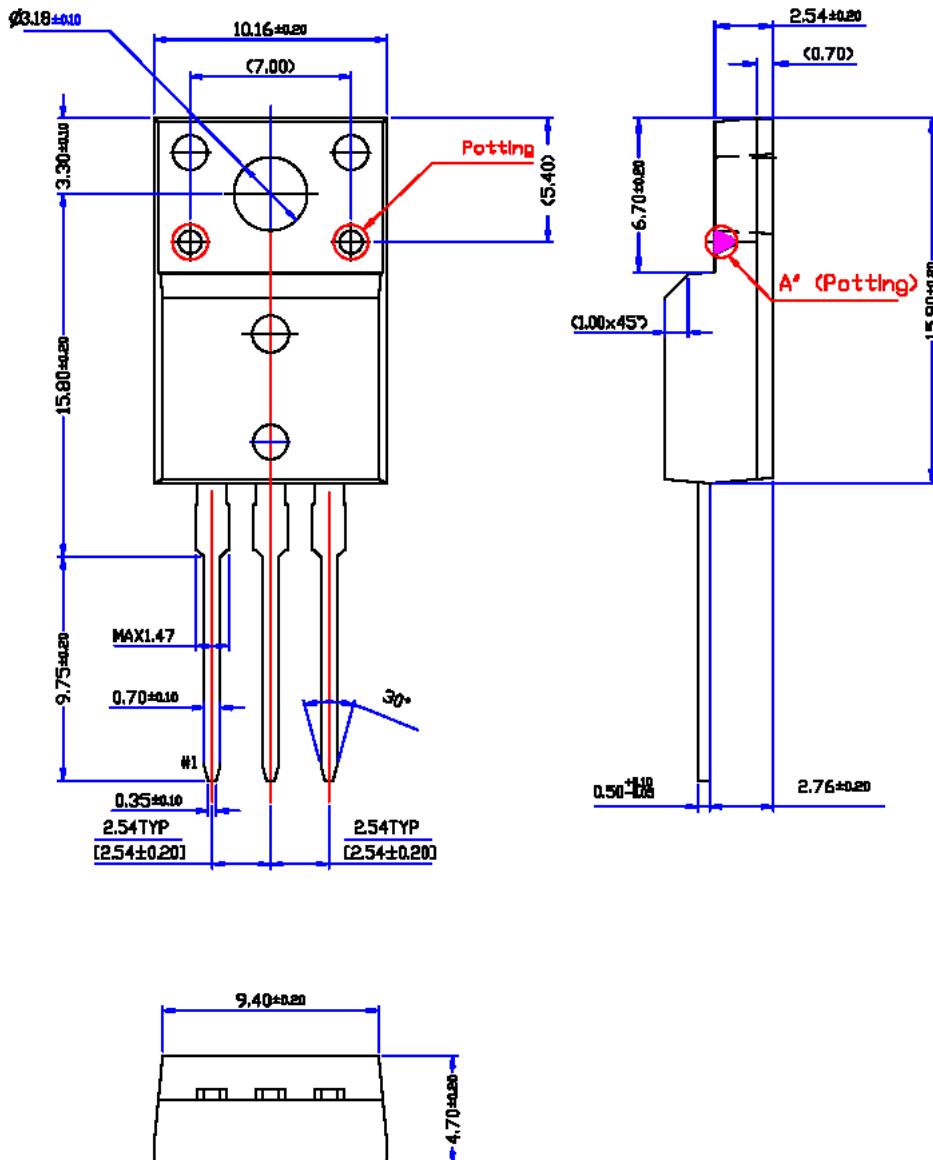
TO-220



Dimensions in Millimeters

Package Dimensions

TO-220F Potted



* Front/Back Side Isolation Voltage : AC 2500V

Dimensions in Millimeters

Dimensions in Millimeters



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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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Rev. I41